## Description

The JS15U1GD50-3 is a 15V Uni-directional TVS diode, utiliz-ing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensi-tive data and power line. The JS15U1GD50-3 complies with the IEC 61000-4-2 (ESD) standard with $\pm 30 \mathrm{kV}$ air and $\pm 30 \mathrm{kV}$ contact discharge. It is assembled into an ultra-small lead-free DFN2020-3 package. The small size and high ESD surge protection make JS15U1GD50-3 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

## Circuit Diagram



Circuit and Pin Schematic

## Features

* 5900 W peak pulse power $(8 / 20 \mu \mathrm{~s})$
* Operating voltage: 15 V
* Ultra low clamping voltage
* One power line protects
* Complies with following standards:
- IEC 61000-4-2 (ESD) immunity test Air discharge: $\pm 30 \mathrm{kV}$

Contact discharge: $\pm 30 \mathrm{kV}$

- IEC61000-4-5 (Lightning) 175A (8/20 $\mu \mathrm{s}$ )
* RoHS Compliant
* Package: DFN2020-3


## Applications

* Fast-charge battery chargers
* Power management system
* Cellular Handsets and Accessories
* Personal Digital Assistants
* Notebooks and Handhelds
* Portable Instrumentation
* Digital Cameras


## Ordering Information

| Part Number | Packaging | Reel Size |
| :---: | :---: | :---: |
| JS15U1GD50-3 | 3000/Tape \& Reel | 7 inch |

## Absolute Maximum Ratings ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified)

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Peak Pulse Power $(8 / 20 \mu \mathrm{~s})$ | Ppk | 5900 | W |
| Peak Pulse Current $(8 / 20 \mu \mathrm{~s})$ | IPP | 175 | A |
| ESD per IEC $61000-4-2$ (Air) | VESD | $\pm 30$ | kV |
| ESD per IEC $61000-4-2$ (Contact) | TJ | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Operating Temperature Range | Tstg | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range |  |  |  |

## Electrical Characteristics ( $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ unless otherwise specified)

| Parameter | Symbol | Test Condition | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Working Voltage | VRWM |  |  |  | 15.0 | V |
| Breakdown Voltage | VBR | $\mathrm{IT}_{\mathrm{T}}=1 \mathrm{~mA}$ | 16 |  | 19 | V |
| Reverse Leakage Current | $\mathrm{I}_{\mathrm{R}}$ | $\mathrm{V}_{\mathrm{RWM}}=15 \mathrm{~V}$ |  |  | 20 | uA |
| Clamping Voltage | Vc | Ipp $=100 \mathrm{~A}(8 \times 20 \mu$ s pulse $)$ |  |  | 27 | V |
| Clamping Voltage | Vc | $\mathrm{Ipp}=175 \mathrm{~A}(8 \times 20 \mu \mathrm{spulse})$ |  |  | 34 | V |
| Junction Capacitance | CJ | $V R=0 V, f=1 \mathrm{MHz}$ |  | 1100 |  | pF |

## Portion Electronics Parameter

| Symbol | Parameter |
| :---: | :--- |
| IT | Test Current |
| Ipp | Maximum Reverse Peak Pulse Current |
| Vc | Clamping Voltage @Ic |



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Typical Performance Characteristics ( $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}^{\circ} \mathrm{C}$ unless otherwise Specified)


Junction Capacitance vs. Reverse Voltage


Peak Pulse Power vs. Pulse Time


Power Derating Curve


Clamping Voltage vs. Peak Pulse Current


IEC61000-4-2 Pulse Waveform


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## DFN2020-3 Package Outline Drawing (Dimensions in millimeters)



## Suggested Land Pattern



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