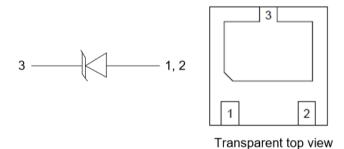


Description

The JS05U1GD50-3 is a high power TVS, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive lines. The JS05U1GD50-3 complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±30kV contact discharge. It is assembled into a 3-pin DFN2020-3 lead-free package. The leads are finished with NiPdAu. Each device will protect one line. The combination of small size, and high surge capability makes them ideal for use in applications such as cellular phones, LCD displays, USB, and multi media card interfaces.

Circuit Diagram



Circuit Diagram

Pin Schematic

Features

- * 5800W peak pulse power (8/20µs)
- * Low leakage: uA level
- Operating voltage: 5V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

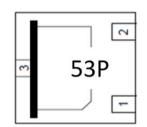
Contact discharge: ±30kV

- IEC 61000-4-5 (Lightning) 280A (8/20μs)
- * RoHS Compliant
- * Package: DFN2020-3

Applications

- * Power Management
- * Industrial Application
- Power Supply Protection

Marking Diagram



Transparent top view

53P:Device Marking Code

Ordering Information

Part Number	Packaging	Reel Size	
JS05U1GD50-3	3000/Tape & Reel	7 inch	



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

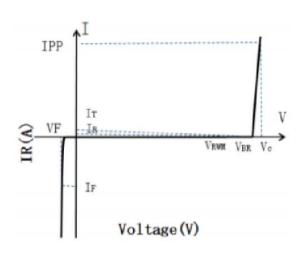
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	5800	W	
Peak Pulse Current (8/20μs)	IPP	280	A	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30		
Operating Temperature Range	TJ	-55 to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				5	V
Breakdown Voltage	V_{BR}	$I_T = 1 \text{mA}$	6			V
Reverse Leakage Current	I_R	$V_{RWM} = 5.0V$			15	μΑ
Clamping Voltage	Vc	$I_{PP} = 100A (8 \times 20 \mu s \text{ pulse})$			13	V
Clamping Voltage	$V_{\rm C}$	$I_{PP} = 280A (8 \times 20 \mu s \text{ pulse})$			21	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		680		pF

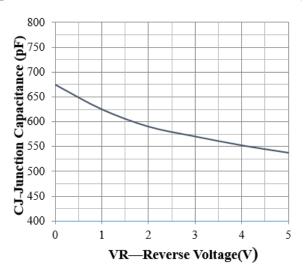
Portion Electronics Parameter

Symbol	Parameter	
Іт	Test Current	
Ірр	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @Ic	

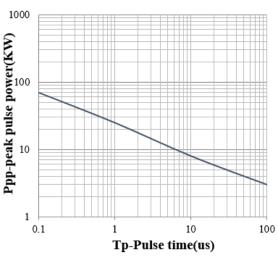




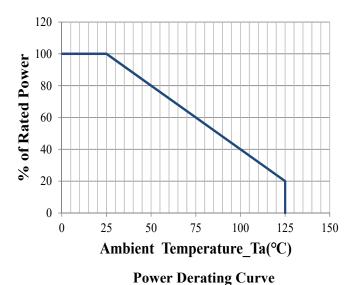
Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



Junction Capacitance vs. Reverse Voltage



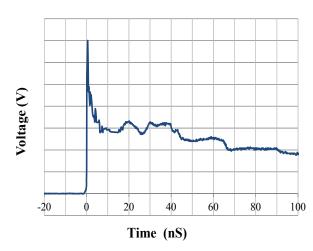
Peak Pulse Power vs. Pulse Time



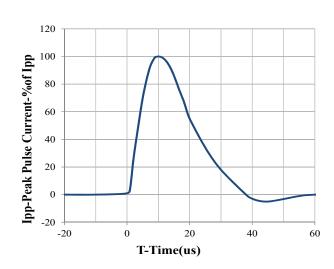
36 Vc-Clamping Voltage (V) 32 28 24 20 16 12 8 4 0 50 100 150 200 250 0 300

Clamping Voltage vs. Peak Pulse Current

IPP—Peak Pulse Current(A)



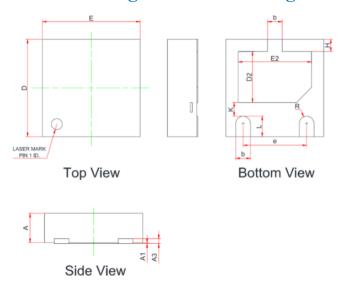
IEC61000-4-2 Pulse Waveform



8 X 20us Pulse Waveform

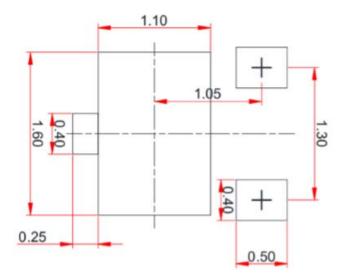


DFN2020-3 Package Outline Drawing



	MILLIMETERS		
SYM	MIN	NOM	MAX
Α	0.55	0.60	0.65
A1	0.00	0.02	0.05
А3	0.10REF.		
b	0.25		0.35
D	1.90		2.10
Е	1.90		2.10
D2	0.95		1.15
E2	1.40		1.60
е	1.20		1.40
Н	0.20		0.30
K	0.20		0.40
L	0.35		0.45
R	0.13		

Suggested Land Pattern



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