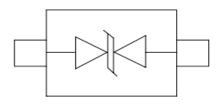


## **Description**

The JE12B1LS30-2 is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers and PDA's, using monolithic silicon technology to provide fast response time and ultra low ESD clamping voltage, making this device an ideal solution for protecting sensitive semiconductor components from damage. The JE12B1LS30-2 complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±30kV contact discharge. The JE12B1LS30-2 is assembled into a lead-free SOD-323 package and will protect one unidirectional line. These devices will fit on the same PCB pad area as an 0805 MLV device.

## **Circuit Diagram**



Circuit and Pin Schematic

# **Marking Diagram**



### **Transparent top view**

12:Device Marking Code

#### **Features**

- \* 500W peak pulse power (8/20µs)
- \* Ultra Low leakage:nA level
- Operating voltage: 12V
- Low clamping voltage
- \* One power line protects
- \* Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 18A (8/20μs)
- \* RoHS Compliant
- \* Package: SOD-323

## **Applications**

- Cellular Handsets and Accessories
- \* Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- \* Peripherals
- \* Pagers Peripherals
- \* Desktop and Servers

# **Ordering Information**

Part Number	Packaging	Reel Size
JE12B1LS30-2	3000/Tape & Reel	7 inch



# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

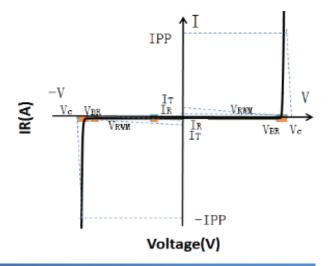
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	500	W
Peak Pulse Current (8/20μs)	IPP	18	A
ESD per IEC 61000-4-2 (Air)	VECD	±30	kV
ESD per IEC 61000-4-2 (Contact)	VESD	±30	K V
Operating Temperature Range	TJ	-55to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				12	V
Breakdown Voltage	VBR	$I_T = 1 \text{mA}$	13.3			V
Reverse Leakage Current	$I_R$	$V_{RWM} = 12V$			0.2	uA
Clamping Voltage	Vc	$I_{PP} = 5A (8 \times 20 \mu s \text{ pulse})$			19	V
Clamping Voltage	Vc	$I_{PP} = 18A (8 \times 20 \mu s \text{ pulse})$			28	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz,			100	pF

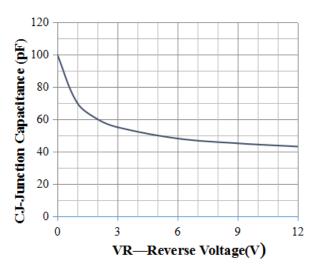
# **Portion Electronics Parameter**

Symbol	Parameter	
Ιτ	Test Current	
Ірр	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @Ic	

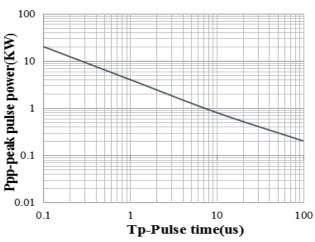




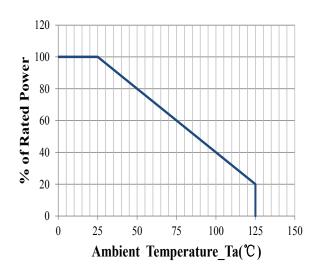
## Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)



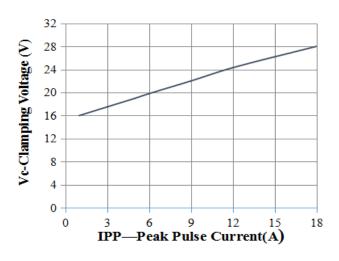
Junction Capacitance vs. Reverse Voltage



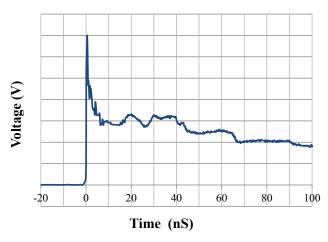
Peak Pulse Power vs. Pulse Time



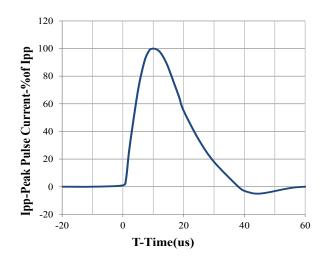
**Power Derating Curve** 



Clamping Voltage vs. Peak Pulse Current



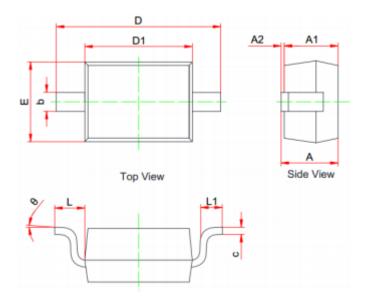
IEC61000-4-2 Pulse Waveform



8 X 20us Pulse Waveform



# SOD-323 Package Outline Drawing (Dimensions in millimeters)



	MILLIMETERS		
SYM	MIN	NOM	MAX
Α	0.800		1.100
A1	0.800		0.900
A2	0.000		0.100
b	0.250		0.400
С	0.080		0.177
D1	1.600	1.700	1.800
D	2.300		2.800
E	1.150		1.400
L			
L1	0.100		0.500
Θ	0°		8°

## **Suggested Land Pattern**



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