

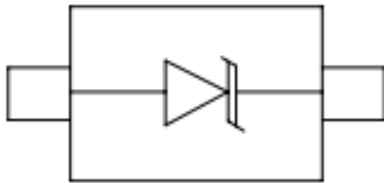
Description

The AU0501D5 is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast re- sponse time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The AU0501D5 complies with the IEC 61000-4-2 (ESD) with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into an ultra-small lead-free SOD package. The small size and high ESD surge protection make AU0501D5 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Features

- * 100W peak pulse power (8/20 μs)
- * Low leakage:nA level
- * Operating voltage: 5V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) 8A (8/20 μs)
- * RoHS Compliant
- * Package: SOD-523

Circuit Diagram



Circuit and Pin Schematic

Applications

- * Keypads, Side Keys, LCD Displays
- * Power management system
- * Cellular Handsets and Accessories
- * Personal Digital Assistants
- * Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras

Marking Diagram



Transparent top view
 D5: Device Marking Code

Ordering Information

Part Number	Packaging	Reel Size
JE05U1LS50-2	3000/Tape & Reel	7 inch

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

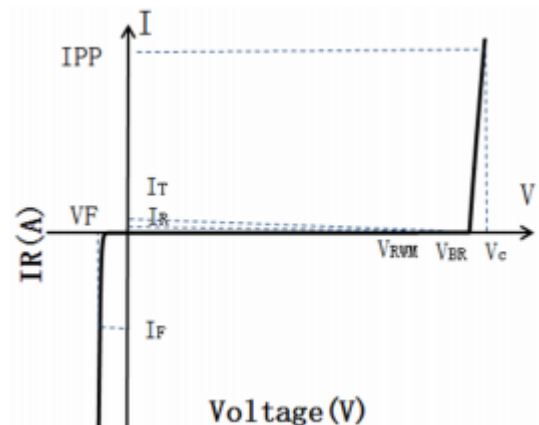
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	100	W
Peak Pulse Current (8/20 μs)	IPP	8	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

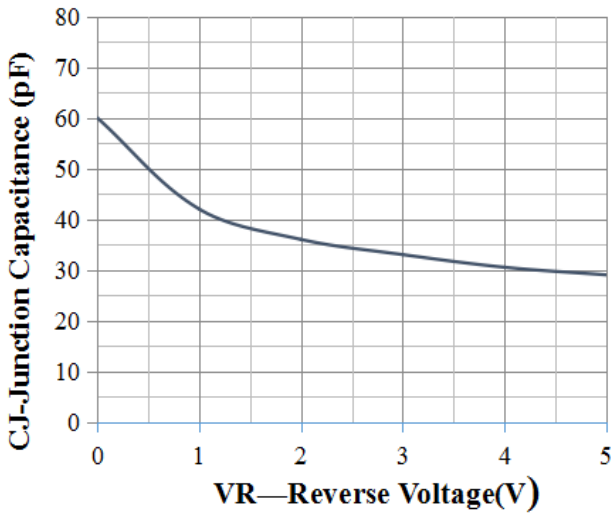
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				5	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	6		8.5	V
Reverse Leakage Current	I_R	$V_{RWM} = 5\text{V}$			0.2	μA
Clamping Voltage	V_C	$I_{PP} = 8\text{A}$ (8 x 20 μs pulse)			12	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		60		pF

Portion Electronics Parameter

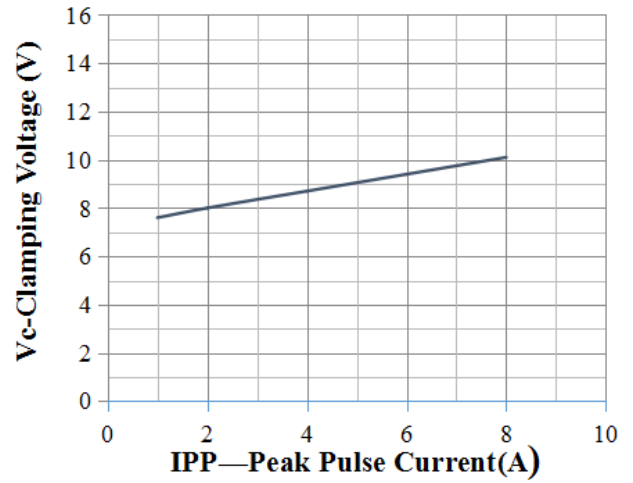
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C



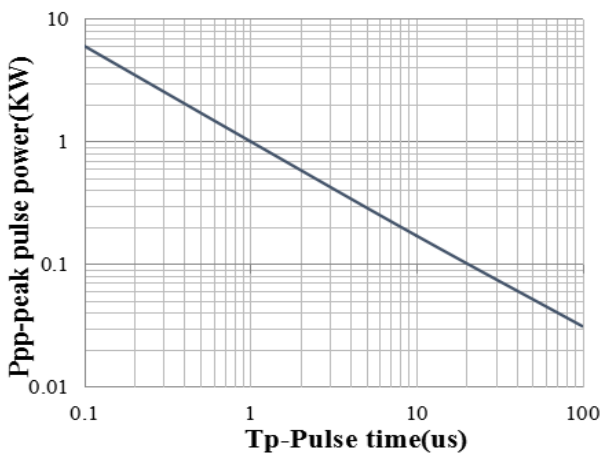
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



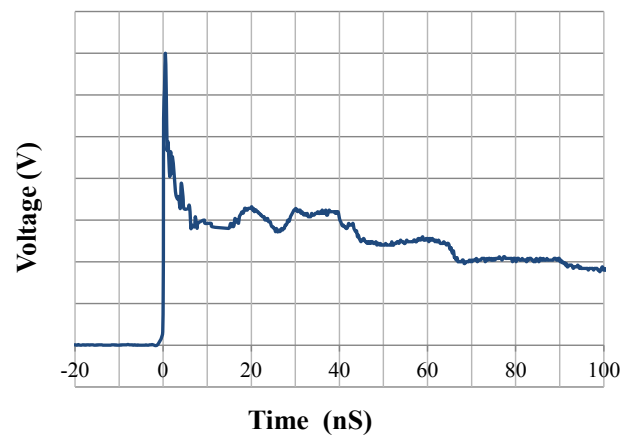
Junction Capacitance vs. Reverse Voltage



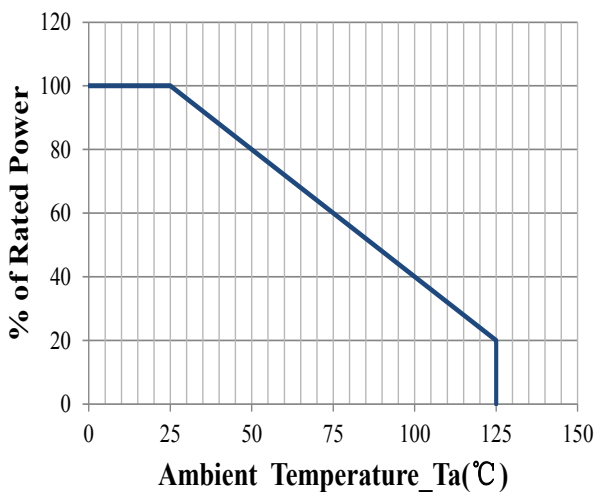
Clamping Voltage vs. Peak Pulse Current



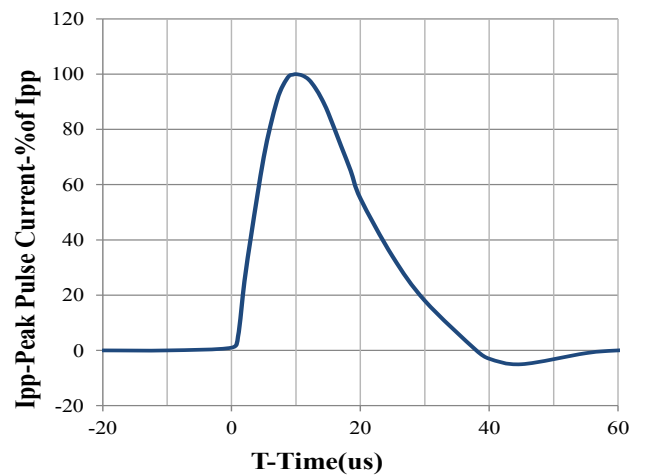
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform

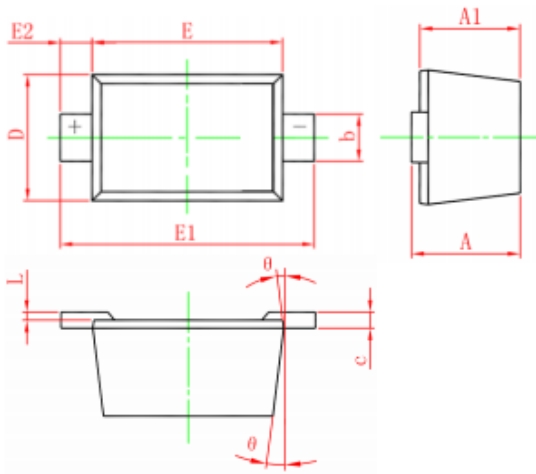


Power Derating Curve



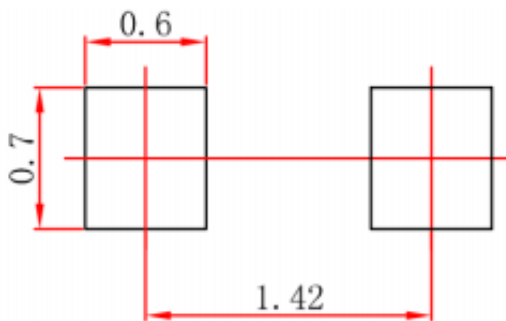
8 X 20us Pulse Waveform

SOD-523 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.51	--	0.77	0.020	--	0.031
A1	0.50	--	0.70	0.020	--	0.028
b	0.25	--	0.35	0.010	--	0.014
c	0.08	--	0.15	0.003	--	0.006
D	0.75	--	0.85	0.030	--	0.033
E	1.10	--	1.30	0.043	--	0.051
E1	1.50	--	1.70	0.059		0.067
E2	0.20REF			0.008REF		
L	0.01	--	0.07	0.001	--	0.003
Θ	7° REF			7° REF		

Suggested Land Pattern



Unit: mm

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