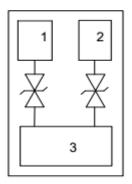


Description

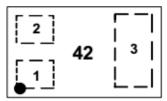
The JE05B2UD20-3A is a 2-line bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The JE05B2UD20-3 complies with the IEC 61000-4-2 (ESD) standard with ± 20 kV air and ± 20 kV contact discharge. It is assembled into an ultra-small 1.0x0.6x0.5mm lead-free DFN package. The small size, and high ESD surge protection make JE05B2UD20-3A an ideal choice to protect cell phone, digital video interfaces, high speed data ports, and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view 42=Device Marking Code

Features

- * 100W peak pulse power (8/20µs)
- * Low leakage: nA level
- Low operating voltage: 5V
- Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±20kV

Contact discharge: ±20kV

- IEC61000-4-5 (Lightning) 7A (8/20μs)
- * RoHS Compliant
- * Package: DFN1006-3

Applications

- * Fast-charge battery chargers
- * Power management system
- * Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- * Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size	
JE05B2UD20-3A	10000/Tape & Reel	7 inch	



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

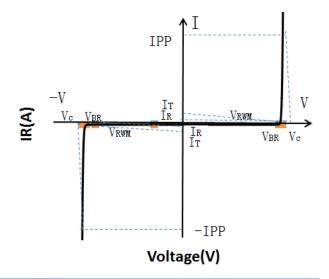
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	100	W	
Peak Pulse Current (8/20µs)	IPP	7	A	
ESD per IEC 61000-4-2 (Air)	VESD	±20	1, 1/	
ESD per IEC 61000-4-2 (Contact)	VESD	±20	kV	
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	VRWM	Pin 1or pin 2 to pin 3 and between pin 1 and pin 2			5	V
Breakdown Voltage	VBR	IT = 1mA,pin 1or pin 2 to pin 3 and between pin 1 and pin 2	6			V
Reverse Leakage Current	I_R	VRWM = 5V,pin 1or pin 2 to pin 3 and between pin 1 and pin 2			50	nA
Clamping Voltage	Vc	IPP = 1A (8 x 20 μ s pulse), pin 1 to pin 3 or pin 2 to pin 3			8	V
Clamping Voltage	Vc	IPP = $7A$ (8 x 20 μ s pulse), pin 1 to pin 3 or pin 2 and pin 3			14	V
Junction Capacitance	Сл	VR = 0V, $f = 1MHz$), pin 1 or pin 2 to pin 3		10	15	pF

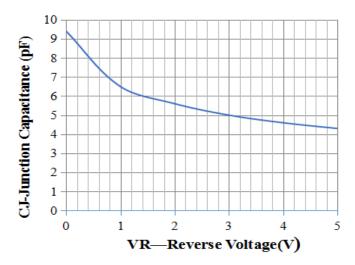
Portion Electronics Parameter

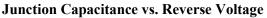
Symbol	Parameter		
IT	Test Current		
Ірр	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @Ic		

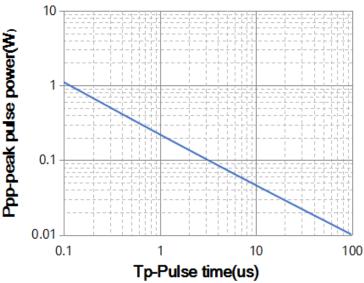


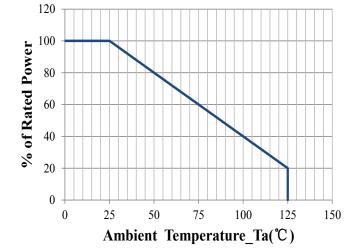


Typical Performance Characteristics (T_A=25°C unless otherwise Specified)

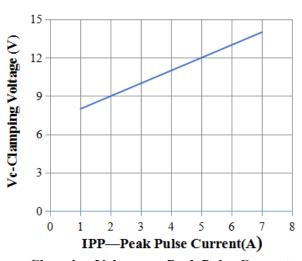




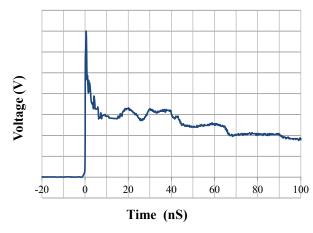




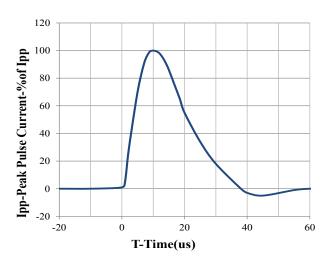
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



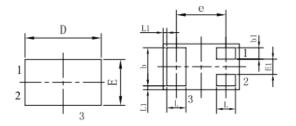
IEC61000-4-2 Pulse Waveform



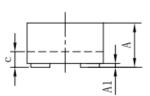
8 X 20us Pulse Waveform



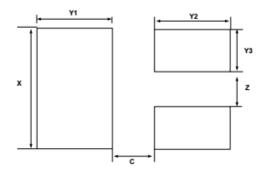
DFN1006-3 Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS					
0.44	MILLIMETERS			INCHES		
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
b1	0.10	0.15	0.20	0.004	0.006	0.008
С	0.12	0.15	0.18	0.005	0.006	0.007
D	0.95	1.00	1.05	0.037	0.039	0.041
е	0.65 BSC)	0	.026 BS	C
E	0.55	0.60	0.65	0.022	0.024	0.026
E1	0.15	0.20	0.25	0.006	0.008	0.010
L	0.20	0.25	0.30	0.008	0.010	0.012
L1		0.05 REF		0.	0002 RE	F



Suggested Land Pattern



0)/14	DIMENS	ENSIONS		
SYM	MILLIMETERS	RS INCHES		
С	0.25	0.010		
Х	0.65	0.024		
Y1	0.50	0.020		
Y2	0.50	0.020		
Y3	0.25	0.010		
Z	0.20	0.008		

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