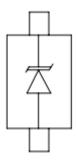


Description

The JE33U1LS50-2 is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and ultra low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JE33U1LS50-2 complies with the IEC 61000-4-2 (ESD) with \pm 30kV air and \pm 30kV contact discharge. It is assembled into an ultra-small SOD-523 lead-free package. The small size and high ESD surge protection make JE33U1LS50-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

3D:Device Marking Code

Features

- * 100W peak pulse power (8/20µs)
- * Low leakage:nA level
- * Operating voltage: 3.3V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 10A (8/20μs)
- * RoHS Compliant
- * Package: SOD-523

Applications

- * Cellular Handsets and Accessories
- * Personal Digital Assistants
- Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras
- Peripherals
- Audio Players
- Keypads,Side Keys,LCD Displays

Ordering Information

Part Number		Packaging	Reel Size	
	JE33U1LS50-2	3000/Tape & Reel	7 inch	



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

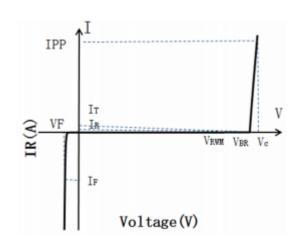
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	100	W	
Peak Pulse Current (8/20μs)	IPP	10	A	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30		
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				3.3	V
Breakdown Voltage	VBR	$I_T = 1 \text{mA}$	3.5			V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3V$			0.2	uA
Clamping Voltage	Vc	$I_{PP} = 1A (8 \times 20 \mu s \text{ pulse})$			7	V
Clamping Voltage	Vc	$I_{PP} = 10A (8 \times 20 \mu s \text{ pulse})$			10	V
Junction Capacitance	CJ	VR = 0V, f = 1MHz,		60		pF

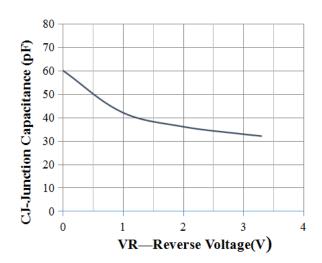
Portion Electronics Parameter

Symbol	Parameter
Ιτ	Test Current
Ірр	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @Ic

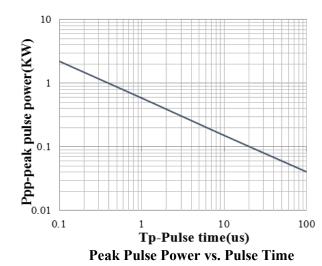


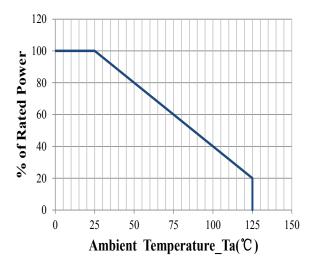


Typical Performance Characteristics (T_A=25°C unless otherwise Specified)

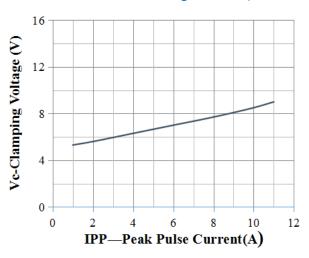


Junction Capacitance vs. Reverse Voltage

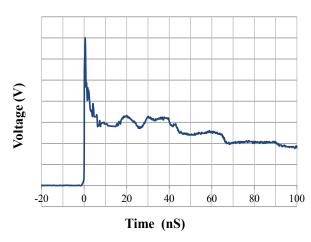




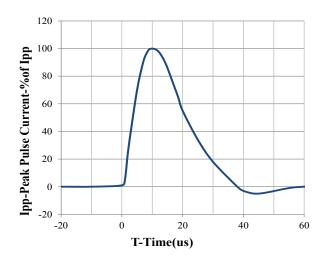
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



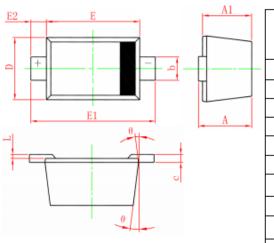
IEC61000-4-2 Pulse Waveform



8 X 20us Pulse Waveform

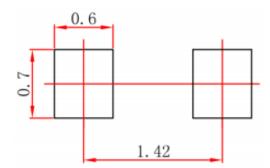


SOD-523 Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS					
SY	MILLIMETERS		ERS	INCHES		
M	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.51		0.77	0.020		0.031
A1	0.50		0.70	0.020		0.028
b	0.25		0.35	0.010		0.014
С	0.08		0.15	0.003		0.006
D	0.75		0.85	0.030		0.033
Е	1.10		1.30	0.043	0.09	
E1	1.50		1.70	0.059		0.067
E2	0.20REF			0.008REF		
L	0.01		0.07	0.001		0.003
Θ	Θ 7° REF			7° REF		

Suggested Land Pattern



单位 (mm)

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