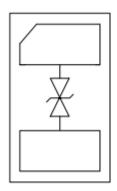


Description

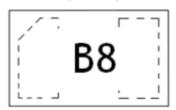
The JE05B1LD20-2 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JE05B1LD20-2 complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±25kV contact discharge. It is assembled into an ultra-small 1.0x0.6x0.5mm lead-free DFN package. The small size and high ESD surge protection make JE05B1LD20-2 an ideal choice to protect cell phone, digital cameras, audio play- ers and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view B8:Device Marking Code

Features

- * 300W peak pulse power (8/20µs)
- Low leakage:nA level
- * Operating voltage: 5V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - -IEC 61000-4-2(ESD)immunity test

Air discharge: ±30kV

Contact discharge: ±25kV

- -IEC61000-4-5(Lightning)25A(8/20us)
- * RoHS Compliant
- * Package: DFN1006-2

Applications

- * Fast-charge battery chargers
- * Power management system
- * Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size
JE05B1LD20-2	10000/Tape & Reel	7 inch



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

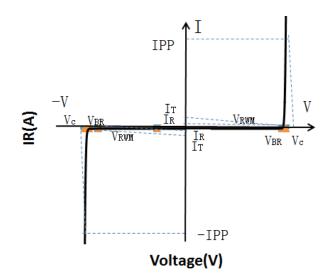
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	300	W	
Peak Pulse Current (8/20μs)	IPP	25	A	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	V ESD	±25		
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				5	V
Breakdown Voltage	VBR	$I_T = 1 \text{mA}$	6	7	9	V
Reverse Leakage Current	I_R	$V_{RWM} = 5V$			0.2	μΑ
Clamping Voltage	Vc	IPP = $1A (8 \times 20 \mu s \text{ pulse})$			7	V
Clamping Voltage	Vc	$I_{PP} = 25A (8 \times 20\mu s \text{ pulse})$			12	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		60		pF

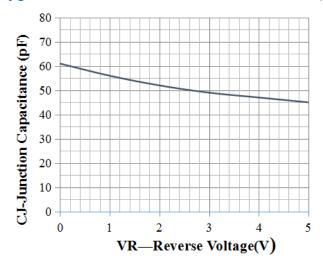
Portion Electronics Parameter

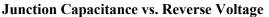
Symbol	Parameter	
IT	Test Current	
Ірр	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @Ic	

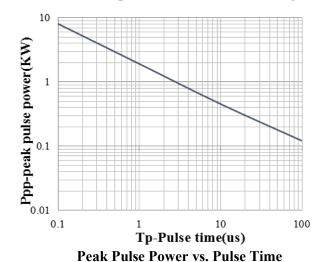




Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



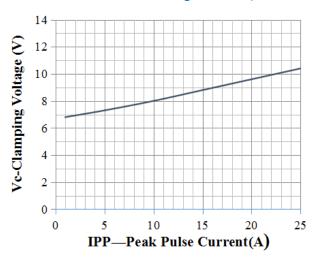




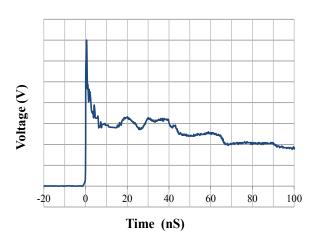
120
100
80
40
20
0
25
50
75
100
125
150

Ambient Temperature_Ta(°C)

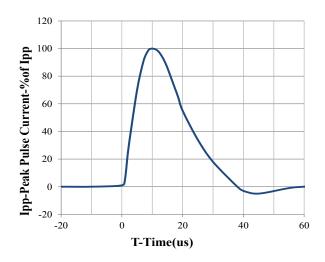
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



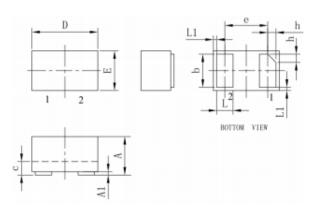
IEC61000-4-2 Pulse Waveform



8 X 20us Pulse Waveform

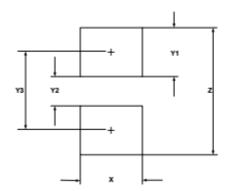


DFN1006-2 Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS					
	MILLIMETERS		INCHES			
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
С	0.12	0.15	0.18	0.005	0.006	0.007
D	0.95	1.00	1.05	0.037	0.039	0.041
е	0.65 BSC		C	0.026 BS	С	
Е	0.55	0.60	0.65	0.022	0.024	0.026
L	0.20	0.25	0.30	0.008	0.010	0.012
L1	0.05REF		(0.002REF	=	
h	0.07	0.12	0.17	0.003	0.005	0.007

Suggested Land Pattern



SYM	DIMENSIONS		
STW	MILLIMETERS	INCHES	
Х	0.60	0.024	
Y1	0.50	0.020	
Y2	0.30	0.012	
Y3	0.80	0.032	
Z	1.30	0.052	

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