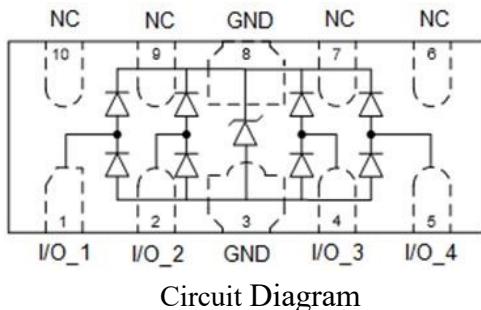


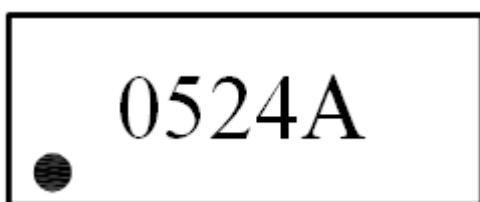
Description

The JE05U4RD40-10A is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The JE05U4RD40-10A has an ultra-low capacitance with a typical value at 0.6pF, and complies with the IEC 61000-4-2 (ESD) standard with $\pm 25\text{kV}$ air and $\pm 20\text{kV}$ contact discharge. It is assembled into a 10-pin 2.5x1.0x0.5mm lead-free DFN package. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0 and HDMI. The small size, ultra-low capacitance and high ESD surge protection make JE05U4RD40-10A an ideal choice to protect HDMI, MDDI, USB 3.0 and other high speed ports.

Circuit Diagram



Marking Diagram



Transparent top view

0524A = Device Marking Code

Dot denotes Pin 1

Features

- * Ultra low capacitance: 0.6pF typical
- * Ultra low leakage: nA level
- * Operating voltage: 5V
- * Low clamping voltage
- * Up to 4 lines protects
- * Leadless flow-through package
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 25\text{kV}$
 - Contact discharge: $\pm 20\text{kV}$
 - IEC61000-4-5 (Lightning) 7A (8/20 μs)
- * RoHS Compliant
- * Package: DFN2510-10

Applications

- * HDMI 1.4& 2.0, USB 2.0 & 3.0 and MDDI ports
- * Monitors and flat panel displays
- * Set-top box and Digital TV
- * Video graphics cards
- * Digital Video Interface (DVI)
- * Notebook Computers
- * PCI Express and Serial SATA Ports

Ordering Information

Part Number	Packaging	Reel Size
JE05U4RD40-10A	3000/Tape & Reel	7 inch

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

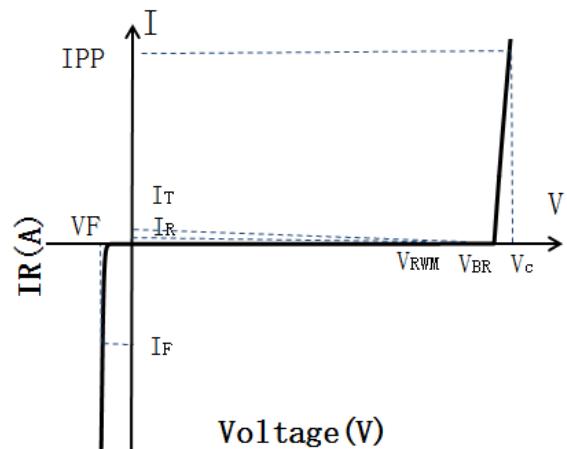
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	60	W
Peak Pulse Current (8/20μs)	IPP	7	A
ESD per IEC 61000-4-2 (Air)	VESD	±25	kV
ESD per IEC 61000-4-2 (Contact)		±20	
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

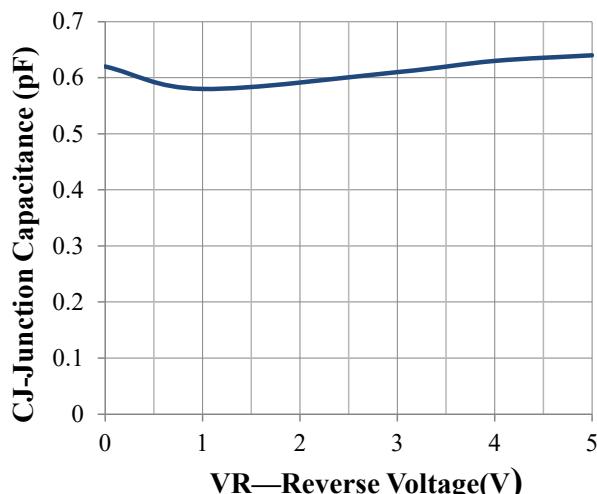
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V _{RWM}				5.0	V
Breakdown Voltage	V _{BR}	I _T = 1mA	6.0	7.5	8.5	V
Reverse Leakage Current	I _R	V _{RWM} = 5.0V			0.5	μA
Clamping Voltage	V _C	IPP = 1A (8 x 20μs pulse)		7.5	8	V
Clamping Voltage	V _C	IPP = 7A (8 x 20μs pulse)		8.0	9	V
Junction Capacitance	C _J	VR = 0V, f = 1MHz		0.6	0.8	pF

Portion Electronics Parameter

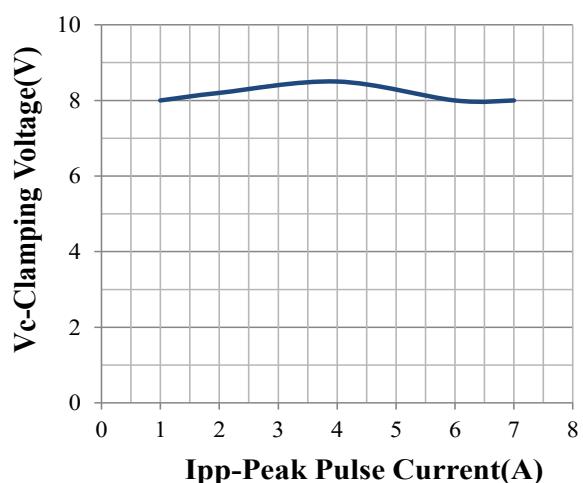
Symbol	Parameter
I _T	Test Current
IPP	Maximum Reverse Peak Pulse Current
V _c	Clamping Voltage @I _c



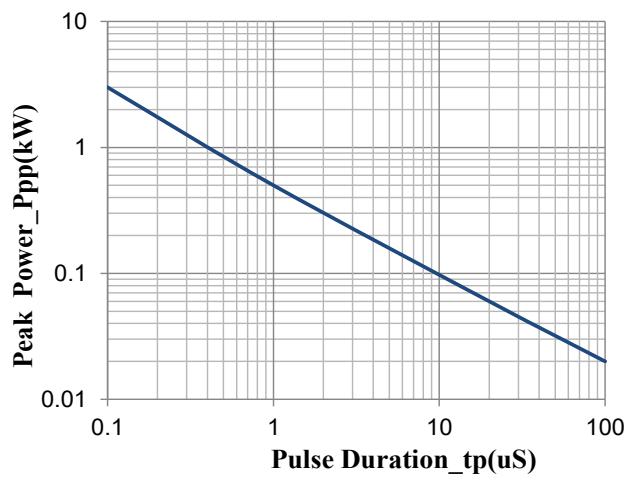
Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)



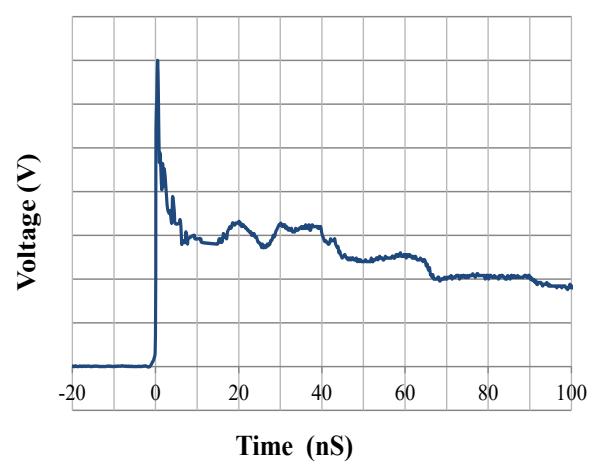
Junction Capacitance vs. Reverse Voltage



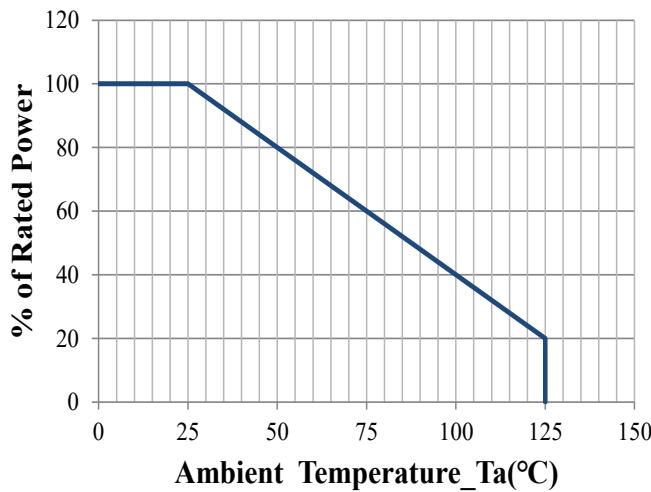
Clamping Voltage vs. Peak Pulse Current



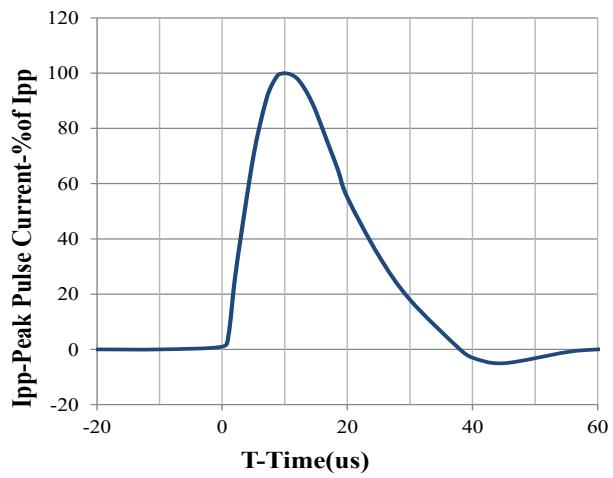
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform

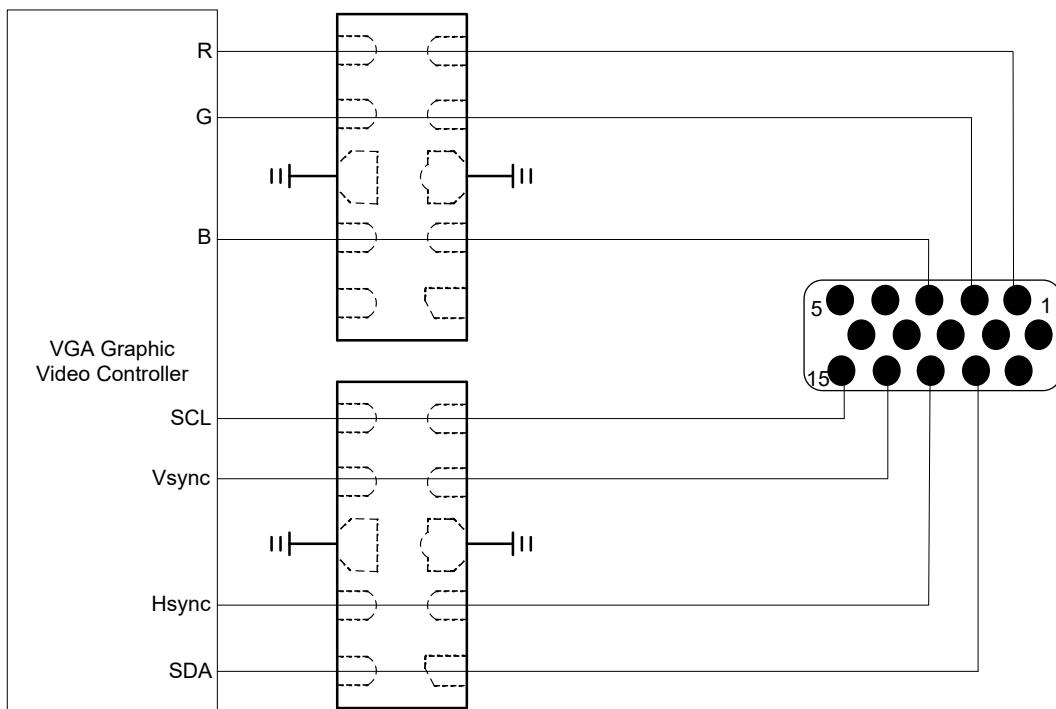


Power Derating Curve

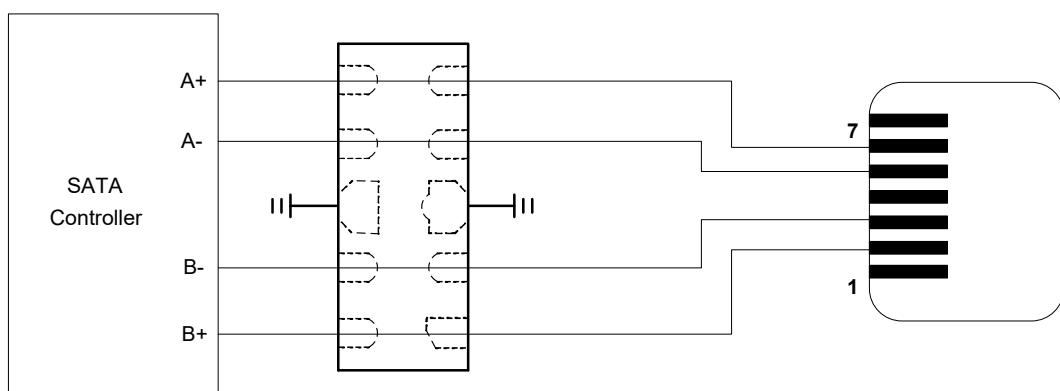


8 X 20us Pulse Waveform

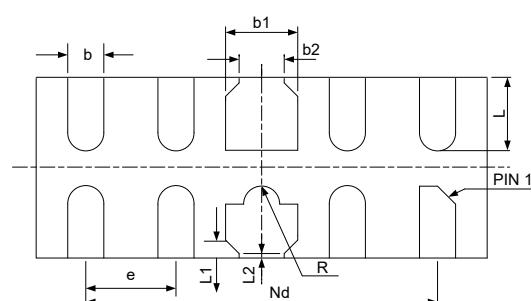
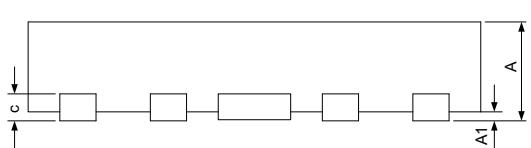
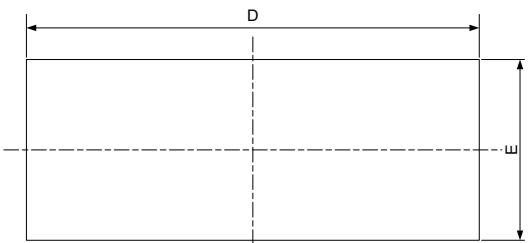
JE05U4RD40-10A on VGA Port Application



JE05U4RD40-10A on eSATA Port Application

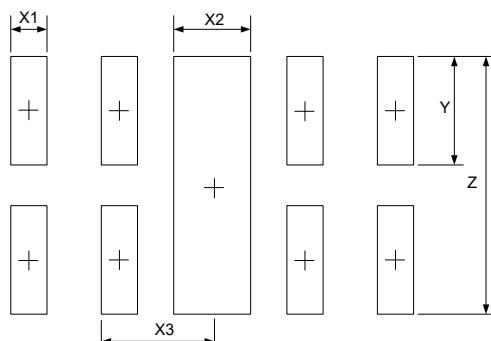


DFN2510-10 Package Outline Drawing



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.15	0.20	0.25	0.006	0.008	0.010
b1	0.35	0.40	0.45	0.014	0.016	0.018
b2	0.20	0.25	0.30	0.008	0.010	0.012
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.45	2.50	2.55	0.098	0.100	0.102
e	0.50BSC			0.020BSC		
Nd	2.00BSC			0.080BSC		
E	0.95	1.00	1.05	0.038	0.040	0.042
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.075REF			0.003REF		
L2	0.050REF			0.002REF		
h	0.08	0.12	0.15	0.003	0.005	0.006
R	0.05	0.10	0.15	0.002	0.004	0.006

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X1	0.200	0.008
X2	0.400	0.016
X3	0.500	0.020
Y	0.600	0.024
Z	1.400	0.056

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